

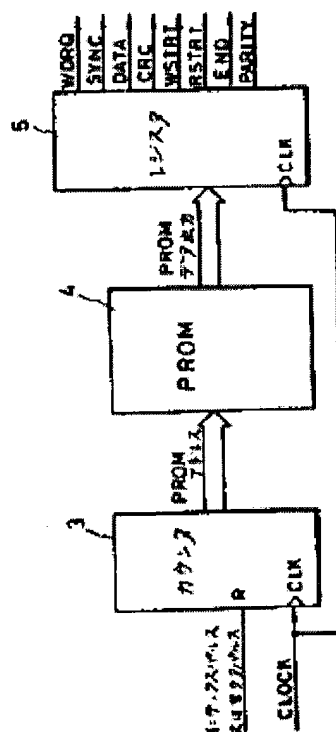
TIMING CONTROL CIRCUIT OF AUXILIARY STORAGE DEVICE

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Inventor: ISHIKAWA TATSUO
Applicant: TOKYO SHIBAURA DENKI KK
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 - international: G06F13/04; G06F1/04; G11B5/09
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Abstract of JP59009765

PURPOSE: To decode a command code without using complicated composite circuit, by providing a PROM in which various command data are stored.

CONSTITUTION: When the recording medium of rotating auxiliary storage device such as a disk pack etc. comes to a reference position, index pulse or selector pulse is inputted to a counter 3. After resetting, this counter 3 counts clock pulse sent out from the auxiliary storage device for each unit information such as byte unit, etc. Every time this counted value is incremented, address value supplied to a PROM 4 is changed, and command data stored in the address are read out and supplied to a register 5. The register 5 receives this command data and outputs corresponding output, for instance, WDRQ (write data request) signal.



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